,	Application No.		Applicant(s)	
Notice of Allowability	09/536,993		SUEHIRO, MITSUO	
	Examiner	er Art Unit		
	Ishwar (I. B.)	Patel	2827	
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIG	(OR REMAINS or other approp GHTS. This ap	) CLOSED in this app priate communication oplication is subject to	olication. If not include will be mailed in due	ed course. <b>THIS</b>
1. This communication is responsive to response filed on Mar	rch 4, 2003.			
2. The allowed claim(s) is/are 8 and 17.				
3. The drawings filed on 30 September 2002 are accepted by the Examiner.				
<ul><li>4.   Acknowledgment is made of a claim for foreign priority und</li><li>a)   All b)   Some* c}   None of the:</li></ul>	ler 35 U.S.C. §	119(a)-(d) or (f).		
<ol> <li>Certified copies of the priority documents have been received.</li> </ol>				
2.   Certified copies of the priority documents have been received in Application No				
<ol> <li>Copies of the certified copies of the priority doc International Bureau (PCT Rule 17.2(a)).</li> </ol>	cuments have b	een received in this r	national stage applica	tion from the
* Certified copies not received:				
5. Acknowledgment is made of a claim for domestic priority un	nder 35 U.S.C.	§ 119(e) (to a provisio	onal application).	
(a) The translation of the foreign language provisional application has been received.				
6. Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.				
Applicant has THREE MONTHS FROM THE "MAILING DATE" of below. Failure to timely comply will result in ABANDONMENT of t  7.  A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which gives reason	this application. itted. Note the	THIS THREE-MON attached EXAMINER	ITH PERIOD IS NOT 'S AMENDMENT or N	EXTENDABLE
<ul> <li>8. ☐ CORRECTED DRAWINGS must be submitted.</li> <li>(a) ☐ including changes required by the Notice of Draftspers</li> <li>1) ☐ hereto or 2) ☐ to Paper No</li> </ul>	son's Patent Dr	awing Review ( PTO-	948) attached	
(b) including changes required by the proposed drawing correction filed, which has been approved by the Examiner.				
(c) 🔲 including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No				
Identifying indicia such as the application number (see 37 CFR 1.	84(c)) should be	written on the drawin	gs in the front (not the	back) of
9. DEPOSIT OF and/or INFORMATION about the depos attached Examiner's comment regarding REQUIREMENT FOR Th				lote the
Attachment(s)				
<ul> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statements (PTO-1449), Paper No</li></ul>		4□ Interview Summa 6⊠ Examiner's Amer	ll Patent Application (I ary (PTO-413), Paper ndment/Comment ment of Reasons for <i>I</i>	No

Application/Control Number: 09/536,993 Page 2

Art Unit: 2827

## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Thomas E. Brown (44,450) on June 18, 2003.

The application has been amended as follows:

- (a) In claim 8, delete "so as to fix" and add - connecting -, line 4-5, and delete "for containing the solder bump", line 7.
  - (b) Cancel claims 10-15, not elected without traverse.
- 2. The following is an examiner's statement of reasons for allowance:

A printed circuit board unit connecting a printed circuit board with an electronic component with an interposer disposed in between with a though hole in the interposer,

wherein the through hole with coating on internal wall in contact with the solder bump, as shown by Tsunoi, US Patent No. 6,429,516 or

through hole without coating, having internal surface of the through hole in the interposer not touching the solder bump, as shown by Ellerson et al., US Patent No. 5,859,470, are known.

Application/Control Number: 09/536,993

Art Unit: 2827

However, printed circuit board unit, with the through hole in the interposer coated

Page 3

with material wet to solder, but not contacting the solder bump, spaced away from the

bump, when assembled, as shown in figure 18 and enabled by the disclosure, has not

been taught or disclosed by the prior art.

Any comments considered necessary by applicant must be submitted no later

than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Ishwar (I. B.) Patel whose telephone number is (703)

305 2617. The examiner can normally be reached on M-F (8:30 - 5).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, David L Talbott can be reached on (703) 305 9883. The fax phone numbers

for the organization where this application or proceeding is assigned are (703) 305 3431

for regular communications and (703) 305 7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or

proceeding should be directed to the receptionist whose telephone number is (703) 308

0956.

Y KAMAND CUNEO

SUPERVISORY PATENT EXAMINER

**TECHNOLOGY CENTER 2860** 

ibp

June 23, 2003